

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	359	bios same stack	USPA T; US-P GPUB	2004/08/3 1 16:30	
2	BRS	L2	887	boot\$8 same stack	USPA T; US-P GPUB	2004/08/3 1 16:31	
3	BRS	L3	1190	1 or 2	USPA T; US-P GPUB	2004/08/3 1 16:31	
4	BRS	L4	1544	stack near5 ram	USPA T; US-P GPUB	2004/08/3 1 16:31	
5	BRS	L5	68	3 and 4	USPA T; US-P GPUB	2004/08/3 1 16:32	
6	BRS	L6	2598	713/1.ccls. 713/2.ccls. 713/100.ccls.	USPA T; US-P GPUB	2004/08/3 1 16:33	
7	BRS	L7	8	5 and 6	USPA T; US-P GPUB	2004/08/3 1 16:46	
8	BRS	L8	60	5 not 7	USPA T; US-P GPUB	2004/08/3 1 16:47	
9	BRS	L9	4501	stack near3 pointer	USPA T; US-P GPUB	2004/08/3 1 16:47	
10	BRS	L10	3264	access\$3 near3 stack	USPA T; US-P GPUB	2004/08/3 1 16:48	
11	BRS	L11	29450	map\$4 near5 (address\$3 or space)	USPA T; US-P GPUB	2004/08/3 1 16:49	
12	BRS	L12	224	multiplex\$3 same stack same ram	USPA T; US-P GPUB	2004/08/3 1 16:50	
13	BRS	L13	2	5 and 12	USPA T; US-P GPUB	2004/08/3 1 16:53	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	21	8 and 9	USPA T; US-P GPUB	2004/08/3 1 17:24	
15	BRS	L15	3	8 and 10	USPA T; US-P GPUB	2004/08/3 1 17:27	
16	BRS	L16	226	4 and 11	USPA T; US-P GPUB	2004/08/3 1 17:28	
17	BRS	L17	34	5 and 16	USPA T; US-P GPUB	2004/08/3 1 17:28	
18	BRS	L18	10	17 not (7 or 13 or 14 or 15)	USPA T; US-P GPUB	2004/08/3 1 17:57	
19	BRS	L19	108	4 and 9 and 11	USPA T; US-P GPUB	2004/08/3 1 17:57	
20	BRS	L20	5	6 and 19	USPA T; US-P GPUB	2004/08/3 1 17:58	